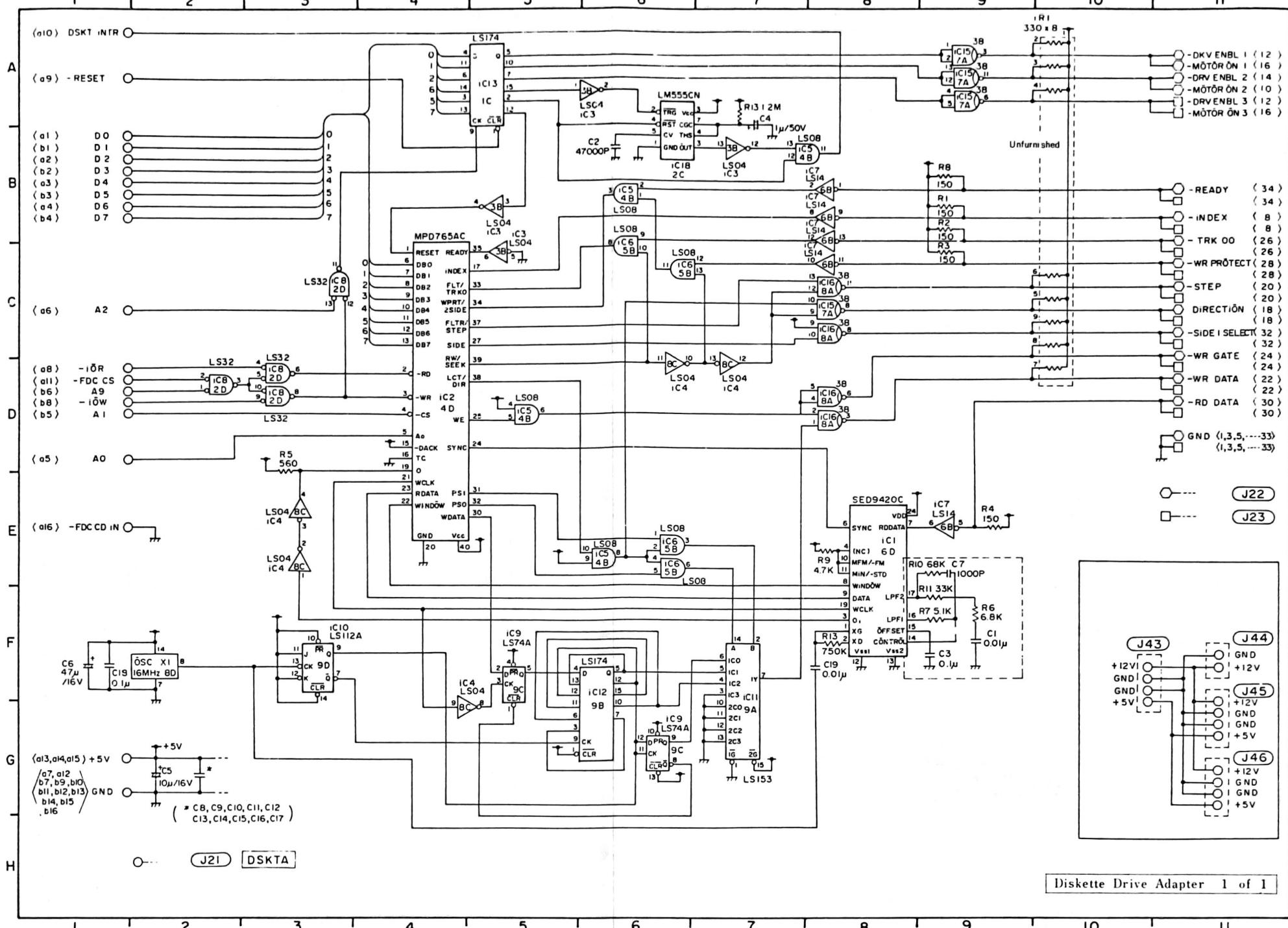
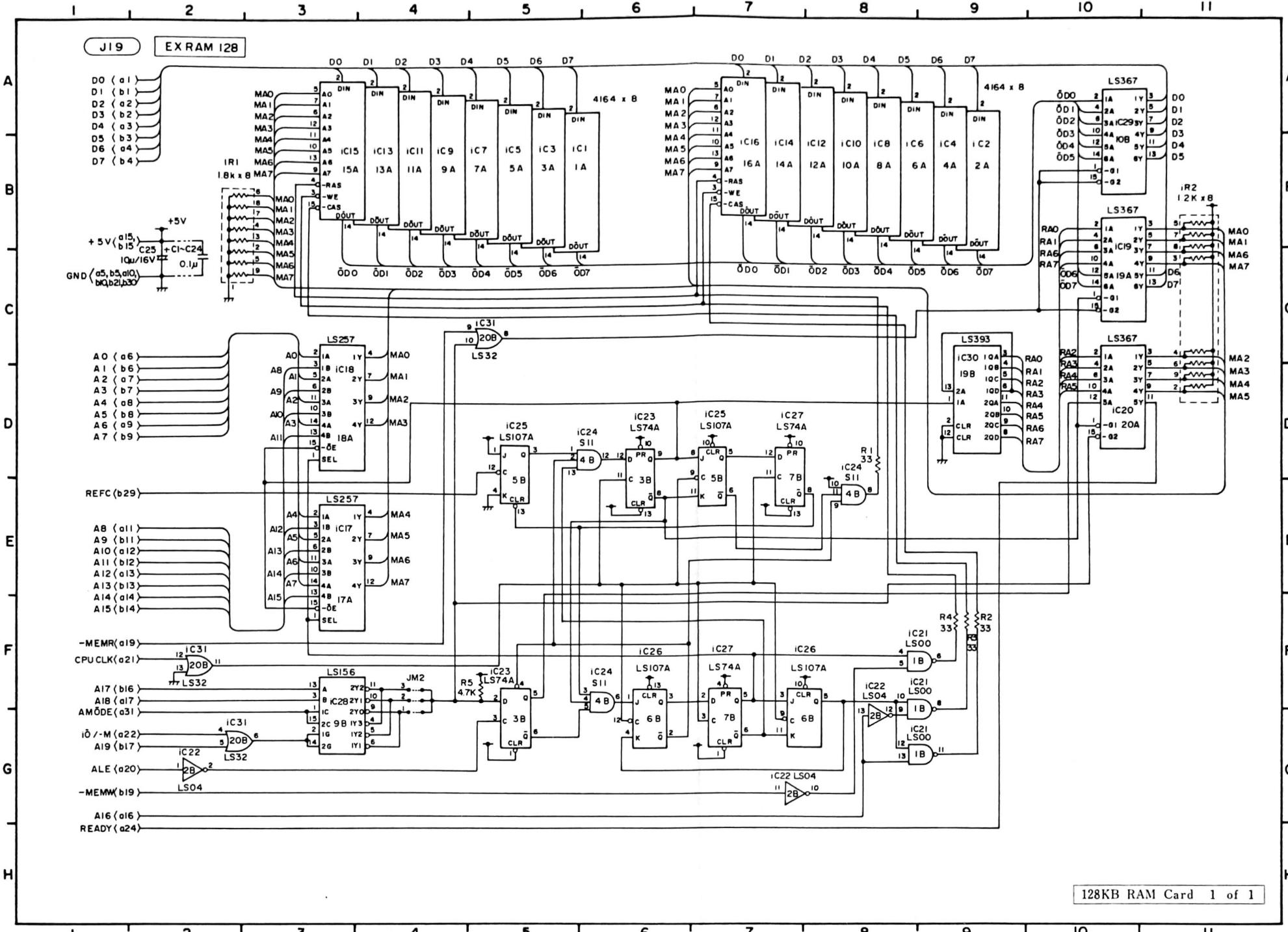
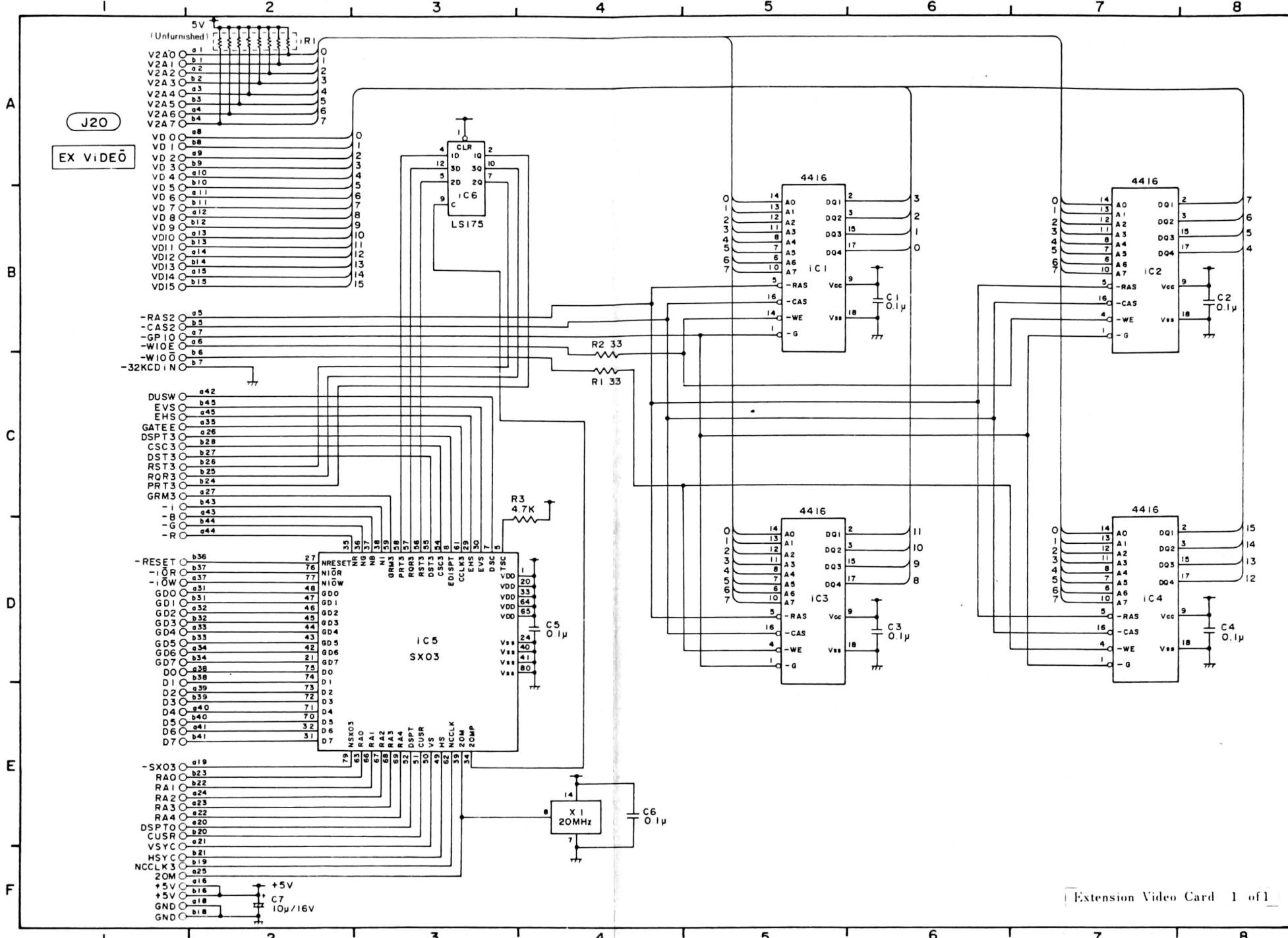


Appendix B. Logic Diagrams





Appendix B. Logic Diagrams



J18 EX RAM 64

EX RAM 64

